IN THE SPECIFICATION:

On page 1, after the title beginning on line 7 and before the first paragraph beginning on line 9. insert the heading:

BACKGROUND OF THE INVENTION

On page 2, after the first paragraph ending on line 9 and before the paragraph beginning on line 11, insert the heading:

SUMMARY

On page 2, after the last paragraph ending on line 30, insert the following heading and paragraph:

BRIEF DESCRIPTION OF THE DRAWING

Fig. 1 is a schematic block diagram of a system in accordance with aspects of the invention.

On page 3, replace the first paragraph beginning on line 1 with the following paragraph:

The present invention will now be described with reference to the figure Fig. 1. The figure

Fig. 1 shows a preferred embodiment of the present invention comprising a switching means 1. Connected to the switching means is a clock oscillator 2. On the left of the figure there are shown two possible clock sources for the normal operation of the emulator system. These clock signals are driven through the backplane of the emulator system onto the IP-Xpress board. The signal gpc2i_clkin is a driven clock from the design, and gpc2i_j17_clkin is a clock directly from the generator of the emulator. Depending on the select signal supplied to the multiplexer 3 either of these clocks is provided to the IP core. For example, the IP core is a DSP. The clock oscillator 2.

Serial No.: 10/501,461 Attorney Docket No.: 006597.00001

the switching means 1 and the signals indicating a breakpoint form the clock generator when

entering the breakpoint mode. The output of the switching means is a clock signal that clocks the IP

core.

On page 3, above line 1, insert the heading:

DETAILED DESCRIPTION OF EXAMPLE EMBODIMENTS

3